

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application : Liang Yan et al.
 Filed : Herewith
 For : INTEGRATED MAGNETIC ISOLATED TWO-INDUCTOR BOOST
 CONVERTER
 Attorney's Docket : NU-625XX

TC Art Unit:

 I hereby certify that this correspondence is being deposited with the
 United States Postal Service as first class mail in an envelope
 addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA
 22313-1450 on _____.

By: _____

James F. Thompson
 Registration No. 36,699
 Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

It is desired to cite for the record in this application the
 enclosed references listed on the attached copy of PTO Form #1449. The
 paragraph(s) marked below are applicable to this Information Disclosure
 Statement.

- [X] (1) Pursuant to 37 C.F.R. § 1.97(b)(1) and (2), the attached
 Information Disclosure Statement is being filed within three
 months of the filing date of the above identified national
 application or within three months of the date of entry of the
 national stage as set forth in 37 C.F.R. § 1.491 of the above
 identified application. Accordingly, applicant(s) believes that
 no fee or statement under 37 C.F.R. § 1.97(e) is required.

Express Mail Number

EV009952794 US

- [] (2) Pursuant to 37 C.F.R. § 1.97(b)(3), applicant(s) believes the attached Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits. Accordingly, applicant(s) believes that no fee or statement under 37 C.F.R. § 1.97(e) is required.
- [] (3) Pursuant to 37 C.F.R. § 1.97(b)(4), applicant(s) believes the attached Information Disclosure Statement is being filed before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Accordingly, applicant(s) believes that no fee or statement under 37 C.F.R. § 1.97(e) is required.
- [] (4) Pursuant to 37 C.F.R. § 1.97(c), the attached Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by:
 - [] a statement under 37 CFR § 1.97(e); or
 - [] the fee set forth in § 1.17(p).

PETITION UNDER 37 CFR § 1.97(d)

- [] (5) Pursuant to 37 CFR § 1.97(d), applicant(s) hereby petitions the Commissioner to consider the attached Information Disclosure Statement which is being filed on or before payment of the issue fee. This petition is accompanied by a statement under 37 C.F.R. § 1.97(e) and the petition fee set forth in 37 C.F.R. § 1.17(p).

STATEMENT UNDER 37 C.F.R. § 1.97(e) (1)

- [] (6) The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

STATEMENT UNDER 37 C.F.R. § 1.97(e) (2)

- [] (7) The undersigned hereby states that no item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to the knowledge of the undersigned after making reasonable inquiry, no item of information contained in the attached Information Disclosure Statement was known to any individual designated in 37 C.F.R. §

1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

Where the status of the application has changed, unknown to the applicant, such that the boxes checked are no longer applicable, the Commissioner is authorized to accept this submission with any additional fees required by that change charged to Deposit Account No. 23-0804.

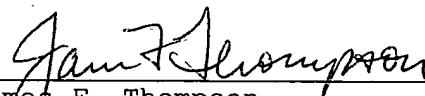
The filing of this Information Disclosure Statement is not a representation by the undersigned as to personal knowledge of the contents of every word or phrase of the material enclosed or that reliance on other suitably trained professionals has not been made.

If a search report of a searching agency is enclosed identifying the nature of the relevance of each document, such a designation is deemed to satisfy 37 C.F.R. § 1.98(a)(3) even if in a foreign language because the codes are the same in all languages. However, applicant(s) does not necessarily adopt the position reflected by that report.

The Commissioner is hereby authorized to charge payment of any additional fees associated with this communication or credit any overpayment to Deposit Account No. 23-0804. Triplicate copies of this letter are enclosed.

Respectfully submitted,

LIANG YAN ET AL.

By: 
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301591-1

Date: February 4, 2004

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				ATTY. DOCKET NO. NU-625XX		APPLICATION NO.	
				APPLICANT: Liang Yan and Bradley M. Lehman			
				FILING DATE Herewith		TC ART UNIT	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION/ ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE
		US 6,549,436	4/15/2003	Sun	363	44	4/15/2003
		US 6,388,896	5/14/2002	Cuk	363	16	3/22/2001
		US 6,362,986	3/26/2002	Schultz et al.	363	132	3/22/2001
		US 6,239,584	5/29/2001	Jang et al.	323	222	1/20/2000
		US 5,784,266	7/21/1998	Chen	363	16	6/14/1996
		US 5,555,494	9/10/1996	Morris	363	17	9/13/1993
		US 5,436,818	7/25/1995	Barthold	363	21	2/14/1994
		US 4,864,478	9/5/1989	Bloom	363	16	12/23/1987
		US 4,853,668	8/1/1989	Bloom	336	214	12/23/1987
		US 3,553,620	1/5/1971	Cielo et al.			12/20/1967
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
OTHER DOCUMENTS <i>(including Author, Title, Date, Pertinent Pages, etc.)</i>							
		Miwa et al., "High Efficiency Power Factor Correction Using Interleaving Techniques," IEEE, pages 557-568, 1992					
		Wolfs, "A Current-Sourced Dc-DC Converter Derived via the Duality Principle from the Half-Bridge Converter," IEEE Transactions on Industrial Electronics, Vol. 40, No. 1, pages 139-144, February 1993					
		Zhu et al., "New Start-up Schemes for Isolated Full-Bridge Boost Converters," IEEE Transactions on Power Electronics, Vol. 18, No. 4, pages 946-951, July 2003					
EXAMINER				DATE CONSIDERED			
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							